

**Notice of References Cited**

Application/Control No.

10/646,612

Applicant(s)/Patent Under  
Reexamination  
COLLINS ET AL.

Examiner

Olivia T. Luk

Art Unit

2812

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,139,697	10-2000	Chen et al.	204/192.15
	B	US-6,020,592	02-2000	Liebert et al.	250/492.21
	C	US-5,510,011	04-1996	Okamura et al.	204/192.3
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	E	US-6,811,448	11-2004	Paton et al.	439/706
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, <u>Pertinent Pages</u>
	U	Stanley Wolf, Silicon Processing for the VLSI Era, Vol. 1, Lattice Press. <u>pages 303-308</u>
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	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.